

ACCELERATED GRAPHICS PORT FOR A MULTIPLE MEMORY
CONTROLLER COMPUTER SYSTEM

Joseph Jeddeloh

Appl. No.: Unknown

Atty Docket: MTIPAT.007C1C1

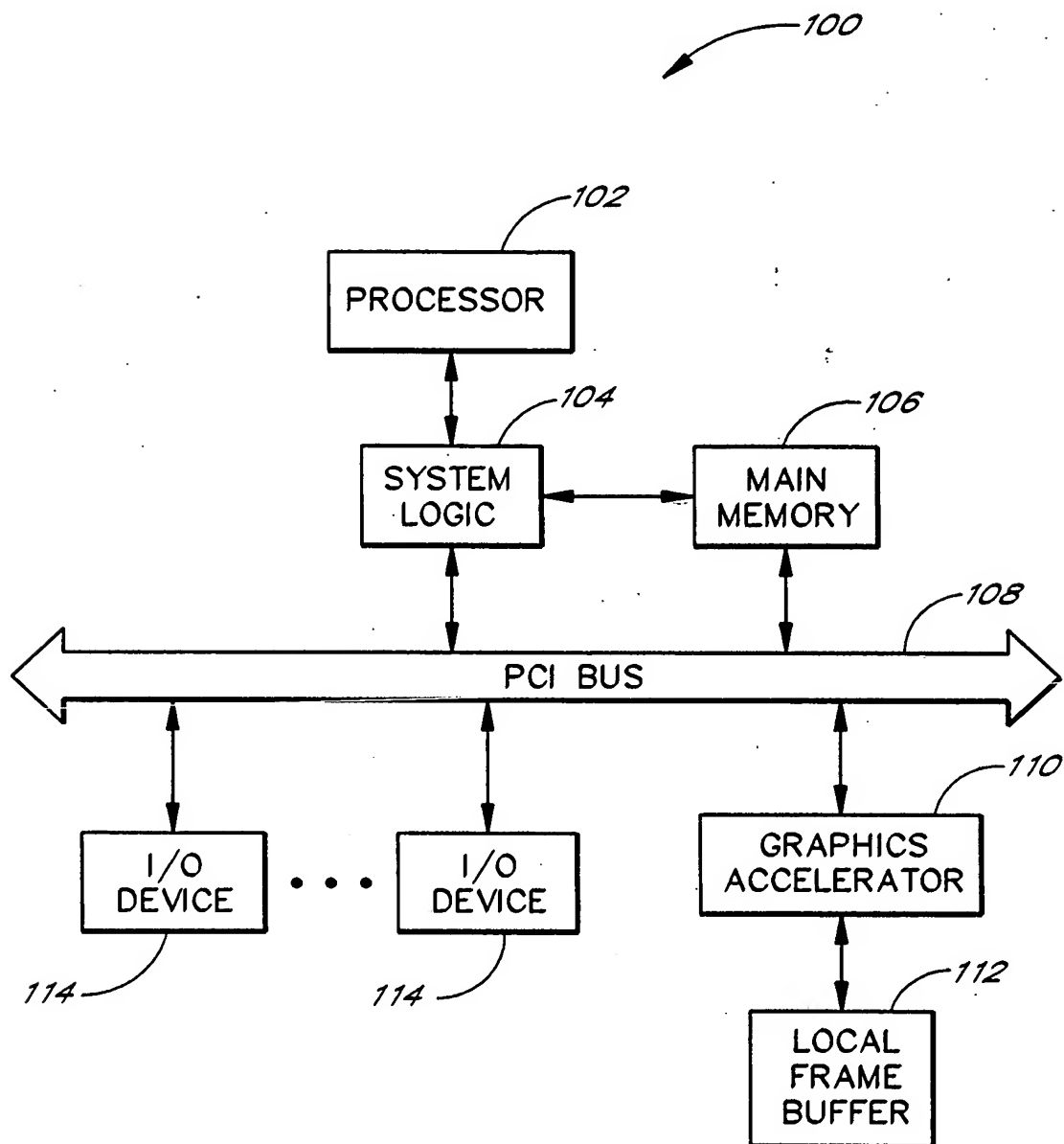


FIG. 1
(PRIOR ART)

ACCELERATED GRAPHICS PORT FOR A MULTIPLE MEMORY
CONTROLLER COMPUTER SYSTEM

Joseph Jeddeloh

Appl. No.: Unknown

Atty Docket: MTIPAT.007C1C1

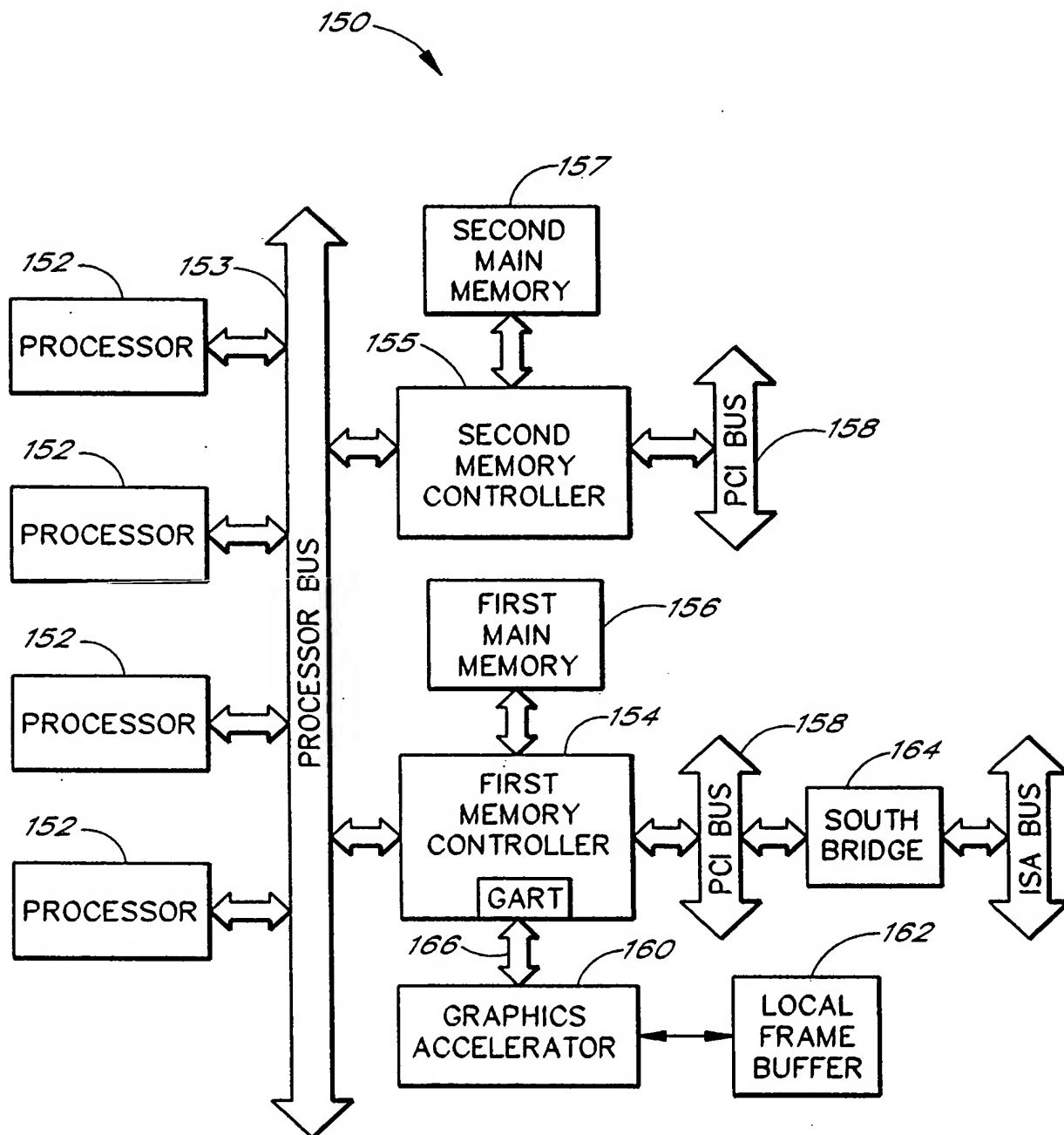


FIG. 2

ACCELERATED GRAPHICS PORT FOR A MULTIPLE MEMORY
CONTROLLER COMPUTER SYSTEM

Joseph Jeddeloh

Appl. No.: Unknown Atty Docket: MTIPAT.007C1C1

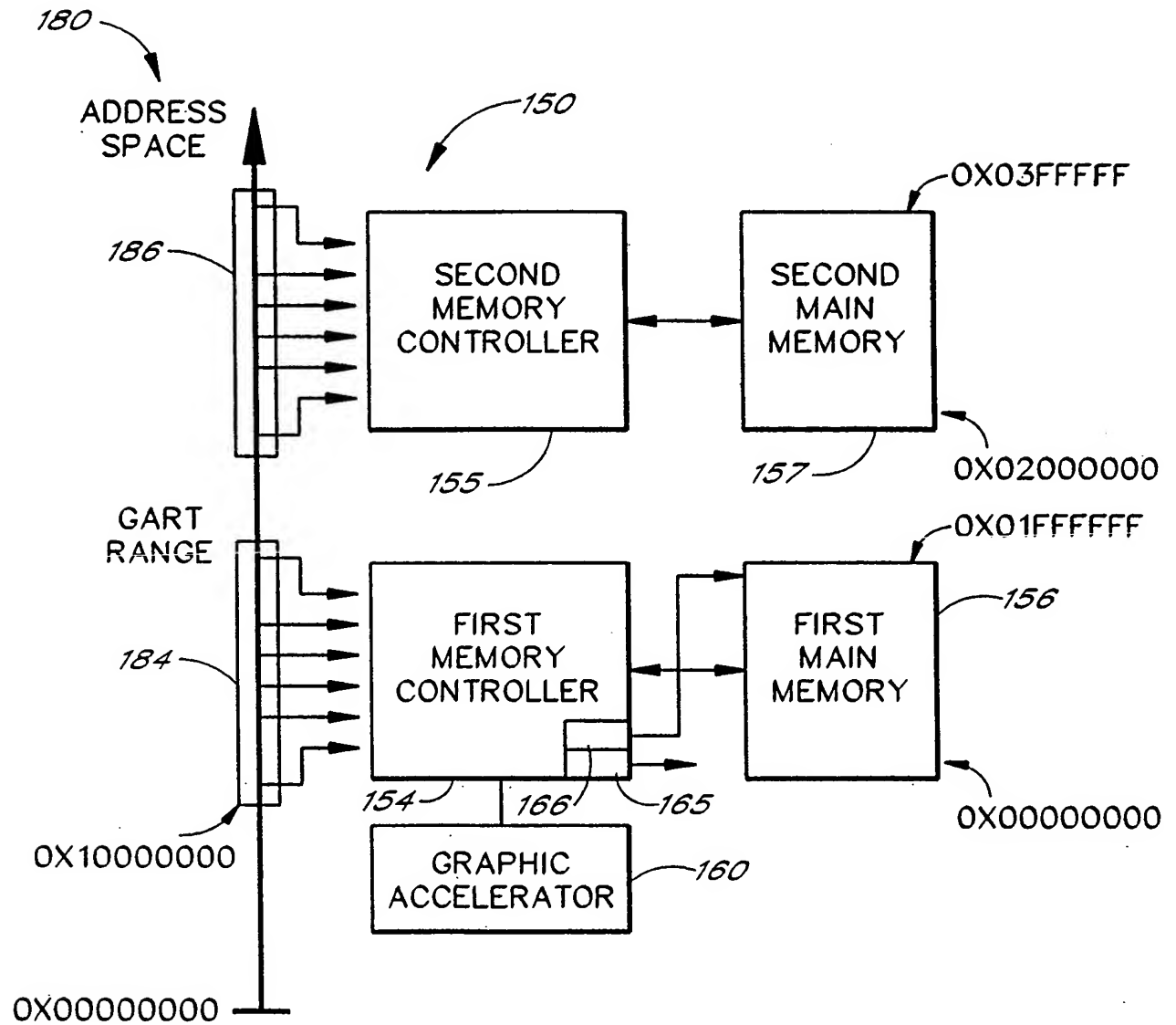


FIG. 3

ACCELERATED GRAPHICS PORT FOR A MULTIPLE MEMORY
CONTROLLER COMPUTER SYSTEM

Joseph Jeddeloh

Appl. No.: Unknown

Atty Docket: MTIPAT.007C1C1

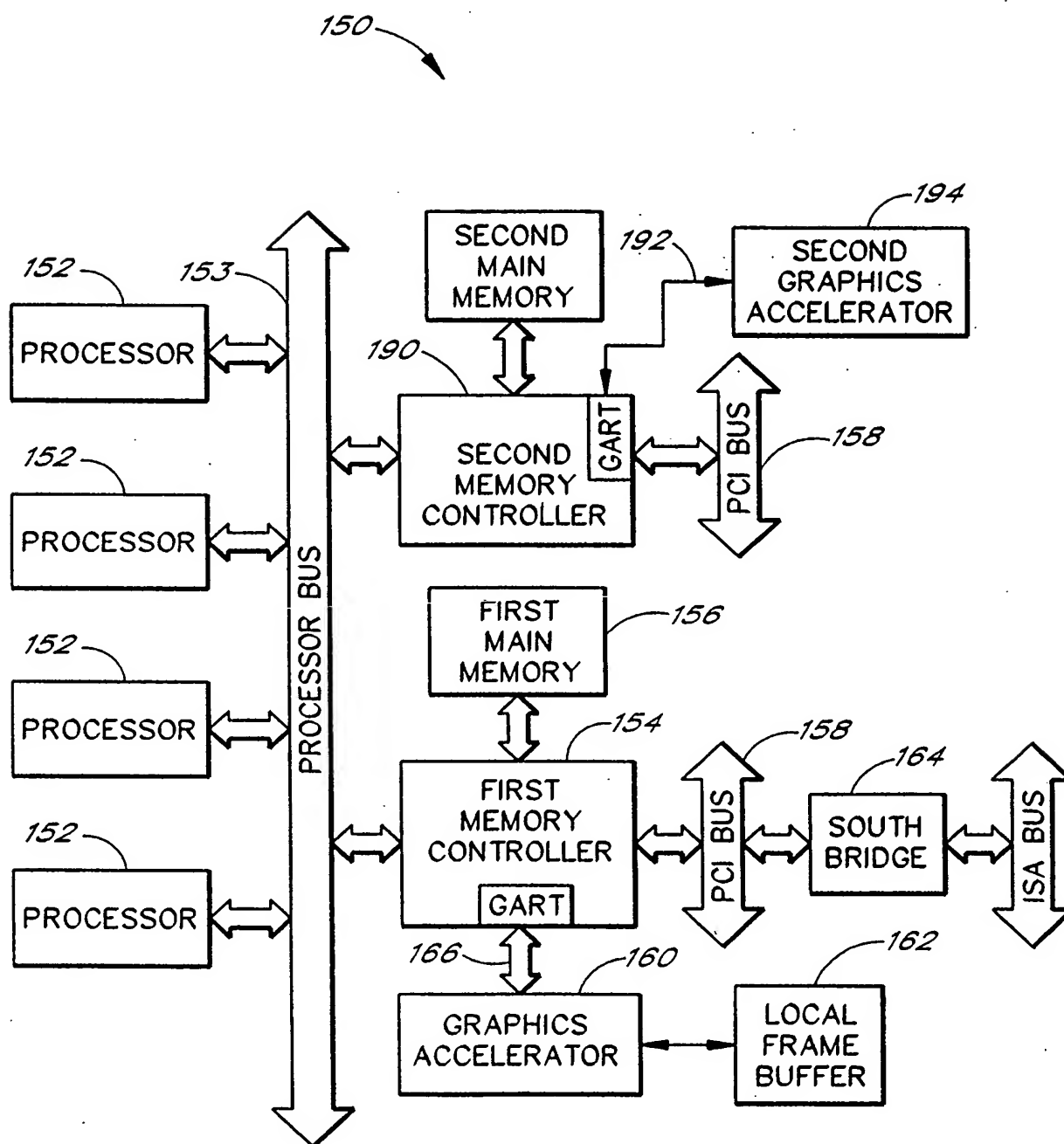


FIG. 4

ACCELERATED GRAPHICS PORT FOR A MULTIPLE MEMORY
CONTROLLER COMPUTER SYSTEM

Joseph Jeddeloh

Appl. No.: Unknown Atty Docket: MTIPAT.007C1C1

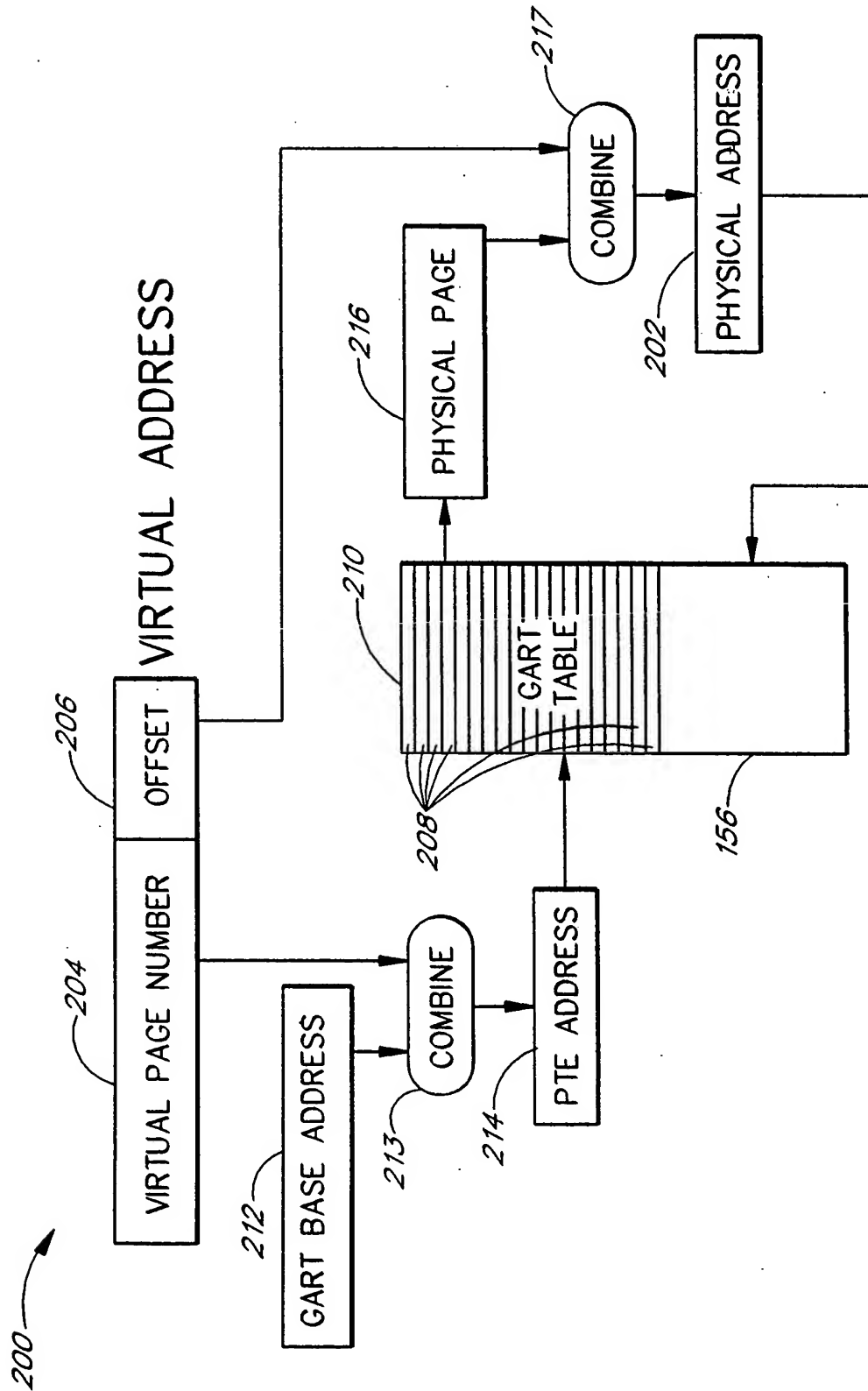


FIG. 5

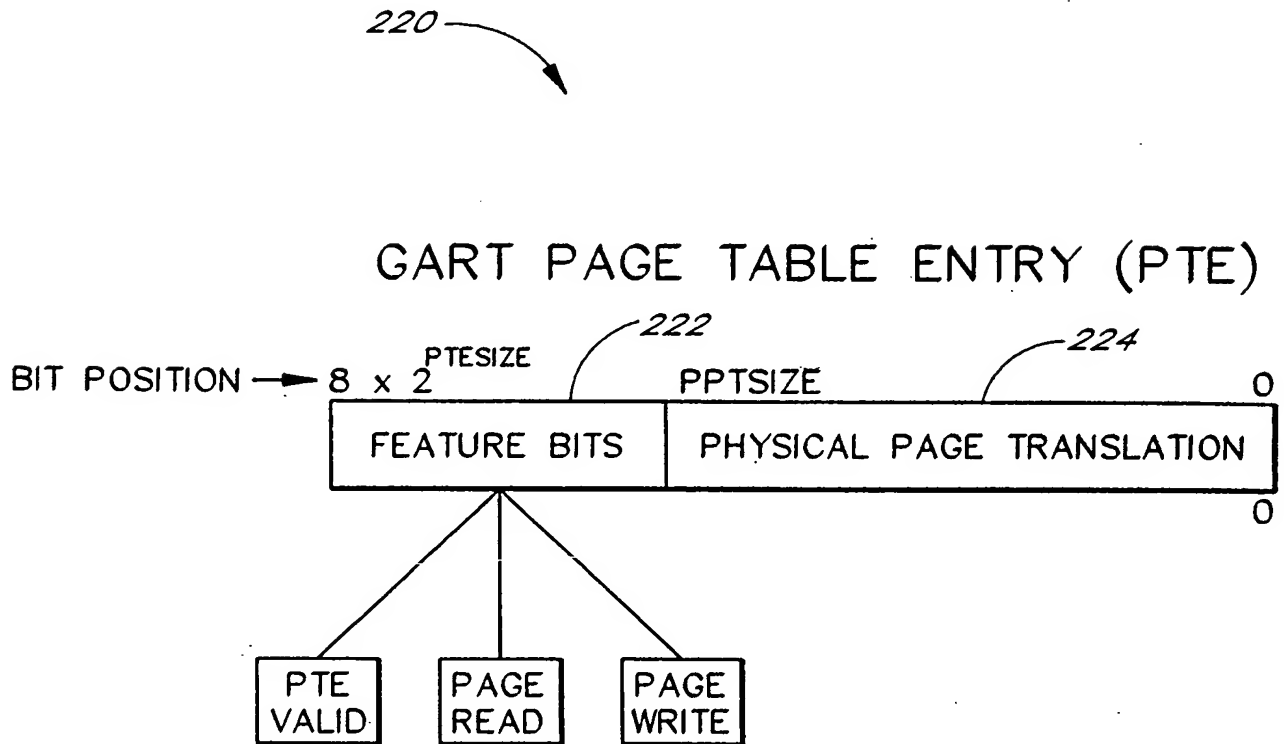


FIG. 6

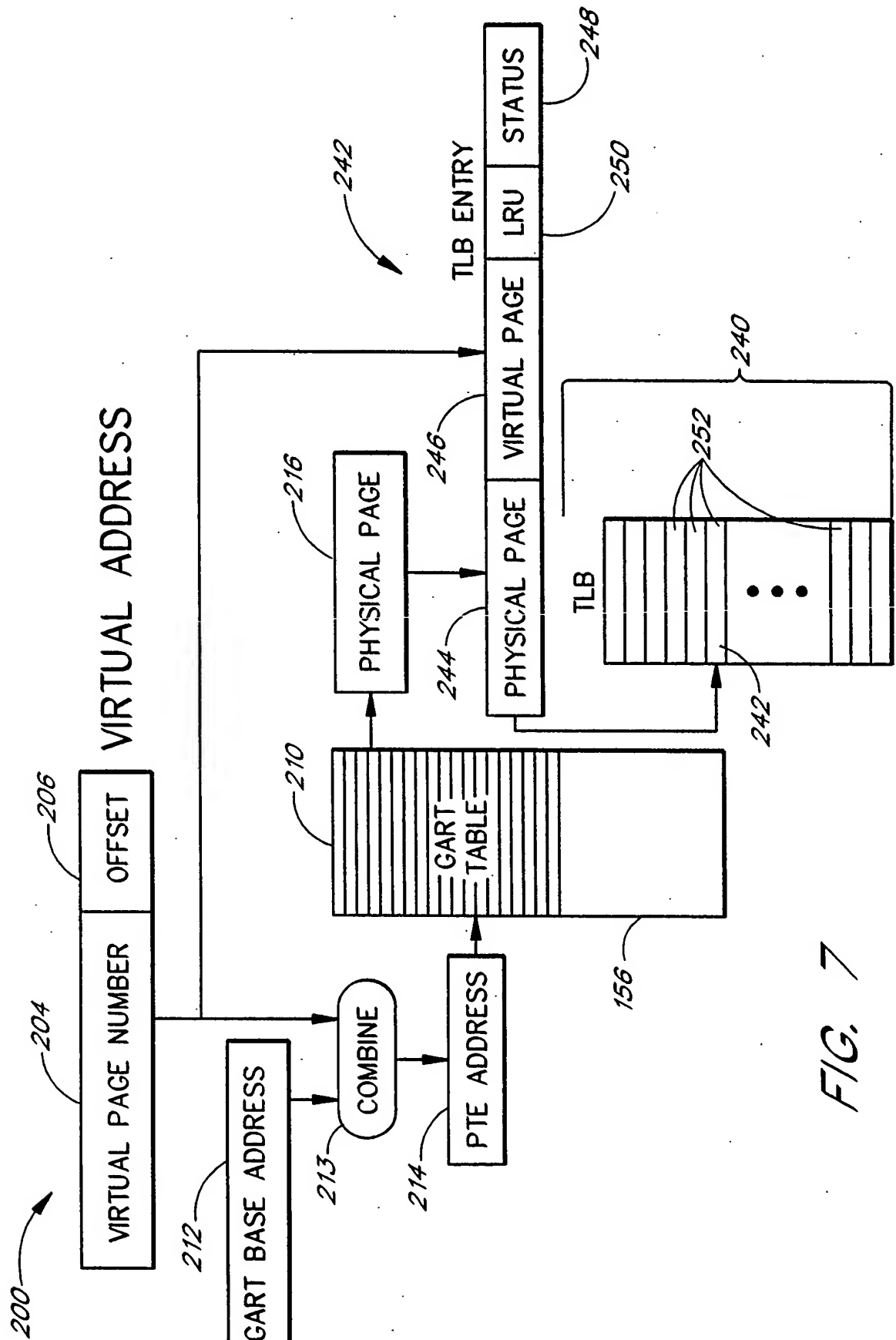


FIG. 7